

**AMENDMENT TRANSMITTAL****PATENT**

Application No. 10/1604  
Filing Date: 01/20/2004  
First Named Inventor Liu, et al.  
Examiner's Name: Elamin, Abdelmoniem I.  
Art Unit: 2116  
Attorney Docket No.: 42P8821C

- ☐ An Amendment After Final Action (37 CFR 1.116) is attached and applicant(s) request expedited action.
- ☒ Charge any fee not covered by any check submitted to Deposit Account No. 02-2666.
- ☒ Applicant(s) hereby request and authorize the U.S. Patent and Trademark Office to (1) treat any concurrent or future reply that requires a petition for extension of time as incorporating a petition for extension of time for the appropriate length of time and (2) charge all required fees, including extension of time fees and fees under 37 CFR 1.16 and 1.17, for any concurrent or future reply to Deposit Account No. 02-2666.
- ☐ Applicant(s) claim small entity status (37 CFR 1.27).

**ATTACHMENTS**

- ☐ Preliminary Amendment
- ☒ Amendment/Response with respect to Office Action
- ☐ Amendment/Response After Final Action (37 CFR 1.116) (reminder: consider filing a Notice of Appeal)
- ☐ Notice of Appeal
- ☐ RCE (Request for Continued Examination)
- ☐ Supplemental Declaration
- ☐ Terminal Disclaimer (reminder: if executed by an attorney, the attorney must be properly of record)
- ☐ Information Disclosure Statement (IDS)
- ☐ Copies of IDS citations
- ☐ Petition for Extension of Time
- ☐ Fee Transmittal Document (that includes a fee calculation based on the type and number of claims)
- ☐ Cross-Reference to Related Application(s)
- ☐ Certified Copy of Priority Document
- ☐ Other: \_\_\_\_\_
- ☐ Other: \_\_\_\_\_
- ☐ Check
- ☒ Postcard (Return Receipt)

**SUBMITTED BY:**

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

TYPED OR PRINTED NAME: Chui-kiu Teresa Wong

SIGNATURE:

REG. NO.: 48,042

DATE: 11/16/2006

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Judy L. Steinkraus

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Judy L. Steinkraus

*11/16/2006*

Application No.: 10/761,604	Filing Date: 01/20/2004	Docket No.: 42P8821C
Date Mailed: 11/16/2006	Due Date: 11/16/2006	Atty/Sec: MJM CTW jxs
Client: Intel Corporation		
Title: OPTIMIZATION OF INTEGRATED CIRCUIT DEVICE I/O BUS TIMING		
First Named Inventor: Liu, et al.		
<i>The following has been received in the U.S.P.T.O. on the date stamped hereon:</i>		
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